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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/707,176	11/25/2003	Kouji Uno	SIC-03-042	1175

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DELAND LAW OFFICE
P.O. BOX 69
KLAMATH RIVER, CA 96050-0069

EXAMINER

TRAN, DALENA

ART UNIT	PAPER NUMBER
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3661

DATE MAILED: 01/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/707,176	Applicant(s) UNO, KOUJI	
	Examiner Dalena Tran	Art Unit 3661	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 25 November 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5, 7, 16-19, 21, 22 and 24 is/are rejected.
- 7) ☒ Claim(s) 6, 8-15, 20, 23 and 25-27 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☒ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>7/15/04, 11/22/04</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Notice to Applicant(s)

1. This application has been examined. Claims 1-27 are pending.
2. The prior art submitted on 7/15/04 and 11/22/04 have been considered.
3. The copy of the foreign priority (Japan 2002-345090 11/28/2002) has not been received yet. Submission is required.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-5,7,16,18-19,21-22, and 24, are rejected under 35 U.S.C.102(b) as being anticipated by Chilcote et al. (4,952,196).

As per claim 1, Chilcote et al. disclose an electronic control device for controlling a controlled device installed on a bicycle, wherein the control device comprises: a programmed computer that controls the control device (see at least columns 31-32, lines 31-22; and columns 53-54, lines 21-20), and a reset circuit that receives information related to a traveling condition of the bicycle and provides a reset signal to the computer in response to the occurrence of a predetermined traveling condition (see at least column 26, lines 3-30; and columns 32-33, lines 23-51).

As per claim 2, Chilcote et al. disclose the traveling condition comprises bicycle speed (see at least columns 42-43, lines 55-16).

As per claim 3, Chilcote et al. disclose the predetermined traveling condition comprises the bicycle speed being below a predetermined (see at least column 43, lines 17-55).

As per claim 4, Chilcote et al. disclose the traveling condition comprises a signal output by an alternating current generator (see at least column 50, lines 25-54).

As per claim 5, Chilcote et al. disclose the traveling condition comprises a frequency output by the alternating current generator (see at least columns 40-41, lines 31-40).

As per claim 7, Chilcote et al. disclose the traveling condition comprises a voltage output by an alternating current generator (see at least column 35, lines 30-68; and column 51, lines 23-55).

As per claims 16, and 18, Chilcote et al. disclose the reset circuit switches a power signal to the computer to a predetermined state in response to the occurrence of the predetermined traveling condition, and wherein the reset signal and the power signal comprise separate signals (see at least columns 26-27, lines 31-10).

As per claim 17, Chilcote et al. disclose the reset circuit switches the power signal off in response to the occurrence of the predetermined traveling condition (see at least columns 29-30, lines 46-41).

As per claim 19, Chilcote et al. disclose the power signal comprises a direct current signal (see at least column 36, lines 1-25).

As per claim 21, Chilcote et al. disclose the reset circuit comprises a reset signal output circuit (see at least column 26, lines 3-30), and a reset activating circuit that activates the reset signal output circuit in response to the occurrence of the predetermined traveling condition (see at least columns 26-27, lines 31-10).

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As per claim 22, Chilcote et al. disclose the reset activating circuit comprises a power switching circuit that switches power to the reset activating circuit to a predetermined state in response to the occurrence of the predetermined traveling condition (see at least columns 26-27, lines 31-10).

As per claim 24, Chilcote et al. disclose the reset activating circuit switches a power signal off to the reset activating circuit in response to the occurrence of the predetermined traveling condition (see at least columns 29-30, lines 46-41).

6. Claims 6,8-15,20,23, and 25-27, are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

. Meyers et al. (5,408,212)

. Soda et al. (5,806,621)

. Yamawaki (6,449,554)

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dalena Tran whose telephone number is 703-308-8223. The examiner can normally be reached on M-F (7:30 AM-5:30 PM), off every other Friday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas Black can be reached on 703-305-8233. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Patent Examiner
Dalena Tran

A handwritten signature in black ink, appearing to read 'Dalena Tran', with a stylized flourish at the end.

January 19, 2005